

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	634	("PLL" or "DLL" or phase adj2 loop or delay adj2 loop) and (replica or dummy) and (duty adj cycle or phase) adj (adjust\$4 or correct\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/05/05 11:34
L2	188	("PLL" or "DLL" or phase adj2 loop or delay adj2 loop) same (replica or dummy) and (duty adj cycle or phase) adj (adjust\$4 or correct\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/05/05 11:17
L3	446	1 not 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/05/05 11:17
L4	1	"6240046".PN.	USPAT; USOCR	OR	OFF	2005/05/05 11:27
L5	1	"6157690".PN.	USPAT; USOCR	OR	OFF	2005/05/05 11:27
L6	1	"6137328".PN.	USPAT; USOCR	OR	OFF	2005/05/05 11:27
L7	1	"5990714".PN.	USPAT; USOCR	OR	OFF	2005/05/05 11:28
L8	1	"5969552".PN.	USPAT; USOCR	OR	OFF	2005/05/05 11:28
L9	1	"5771264".PN.	USPAT; USOCR	OR	OFF	2005/05/05 11:28
L10	31	("PLL" or "DLL" or phase adj2 loop or delay adj2 loop) and (replica or dummy) and (duty adj cycle) adj (adjust\$4 or correct\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/05/05 11:34

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	6072	voltage adj comparator same transistor\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/05/05 15:55
L2	908	1 and "327"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/05/05 15:55
L3	68	2 and duty adj cycle	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/05/05 15:55